Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	87335	(semiconductor or integrated adj circuit) and (multiplex$5 or mux$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/02 09:19
L2	18873	1 and (comparator$1 or compair$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/02 09:20
L3	7351	2 and resistors	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/02 09:21
L4	2	3 and positive near transmit$4 near pin$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/02 09:23
L5	86	3 and transmit$4 near pin$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/02 09:23
L6	61	5 and test$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/02 09:24